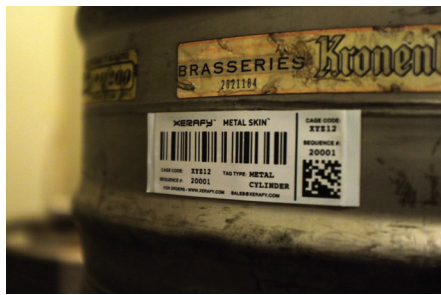


## Xerafy Mercury Metal Skin™ Smart Label EPC UHF RFID Label for Metallic Assets

Xerafy's patent pending Mercury Metal Skin is a revolutionary RFID smart label for both metallic and non-metallic assets. The label can incorporate human readable printing, barcodes, and graphics and is extremely low profile and flexible to fit curved surfaces – such as the contour of metallic cylinders and drums. Globally compliant to EPCglobal UHF Gen2 and ISO 18000-6C standards, Mercury Metal Skin is cost effective and versatile for a wide range of applications from product authentication and IT asset tracking, to global tracking of assets shipped and anti-counterfeiting of high value items and medical supplies.



### Features:

- High performance, cost effective label for metallic assets
- Performs reliably on and off metal
- Lightweight and flexible with paper-thin profile



## Mercury Metal Skin™ Smart Label

The Mercury Metal Skin Smart Label is a high performance, cost efficient RFID metal tag that is lightweight, flexible and versatile.

### Applications:

Product authentication  
IT asset and laptop tracking  
Global supply chain management  
Cylinder tracking  
Foil-based packaging

### Specifications:

EPC Class 1 Gen 2 (ISO 18000-6C)	128-bit user memory
Passive UHF RFID transponders	48-bit serialized TID; 496-EPC bits
Frequency:	860-960 MHz (Global)
Read range on metal (2W ERP):	Up to 13 ft (4 m)
Read range off metal (2W ERP):	Up to 16 ft (5 m)
Operating temperature:	-40°F to +149°F (-40°C to +65°C)
Application temperature:	-40°F to +149°F (-40°C to +65°C)
Dimensions / tolerance (mm)	101.6(+/- 1) x 38(+/- 0.8) x 0.76(+/- 0.1)
Dimensions / tolerance (in)	4(+/- 0.04) x 1.5(+/- 0.03) x 0.03(+/- 0.004)
Quantity per reel:	500
Inner core diameter:	3 in (76.2 mm)
Outer reel diameter:	8 in (203 mm)
Weight (reel):	3.5 lbs (1.6 kg)
Compliance:	RoHS, CE

\*This product utilizes an Impinj M4 series chip; each tag will have the exact same EPC number and a unique TID number. Xerafy will verify the Read/Write function of each chip memory before shipment.

